



FIG. I (PRIOR ART)

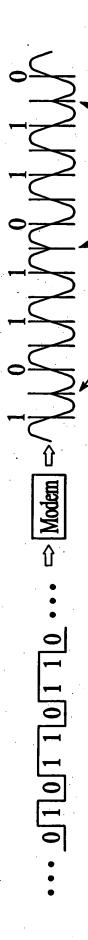


FIG. 2 (PRIOR ART)

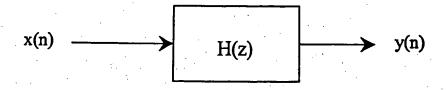


FIG. 3

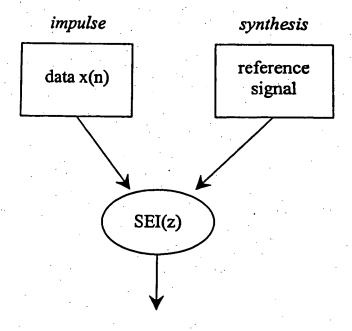
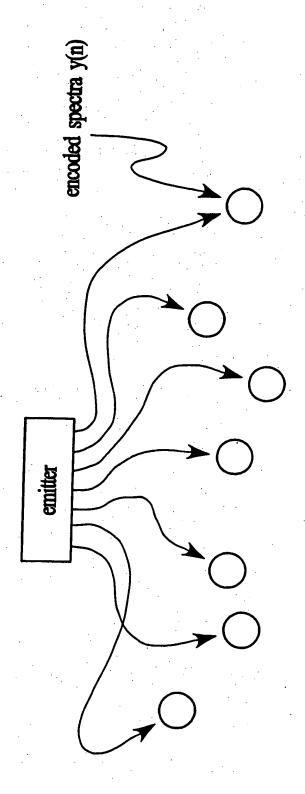


FIG. 4



A more detailed view of an SEI system. Encoded spectra are emitted over time, flowing forward in this direction

FIG. 5

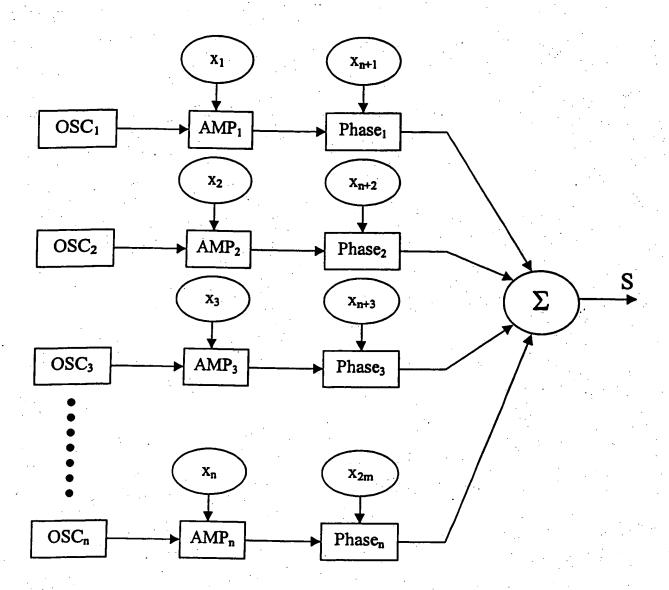


FIG. 6

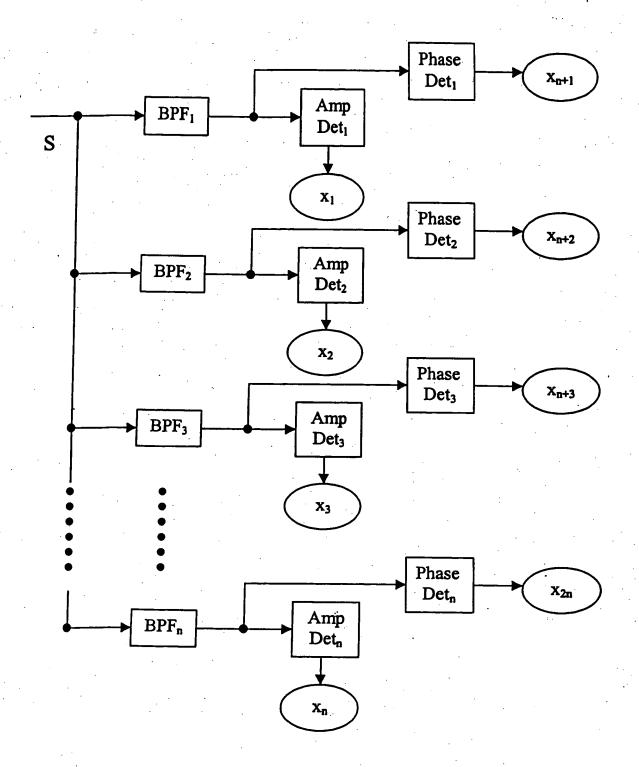


FIG. 7

;}

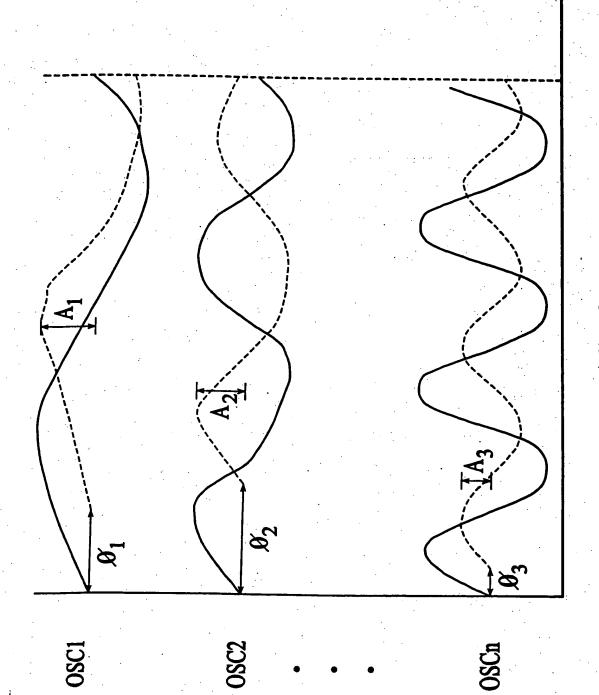
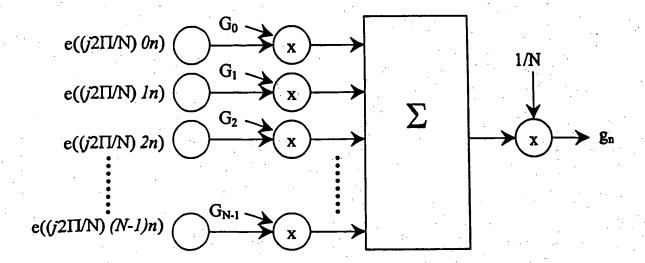


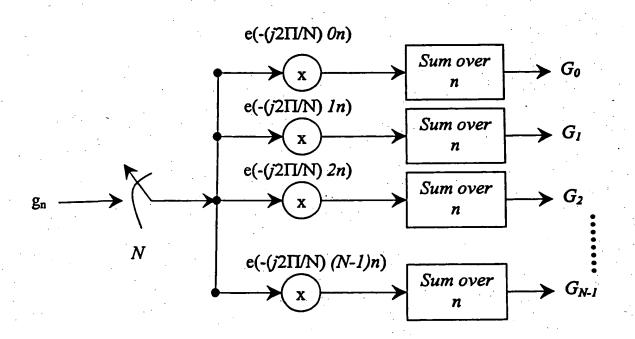
FIG. 8

7		0		
	<b>x</b> <sub>1</sub>		] )	
	X <sub>2</sub>			
	Х3	٠		•
	•			<b>V</b> 0 <b>D</b>
	X <sub>n</sub>		B	X2n – Byte Buffer
	X <sub>n+1</sub>			2 42102
	•			
	X <sub>2n</sub>			

FIG. 9



The Inverse Discrete Fourier Transform



The Discrete Fourier Transform

FIG. 10

144

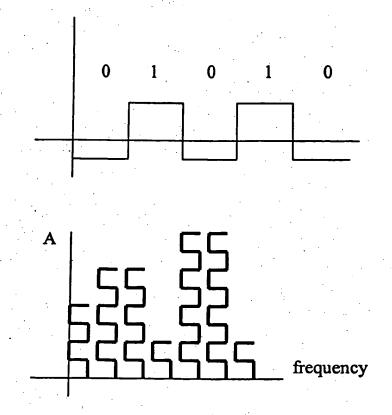


FIG. 11

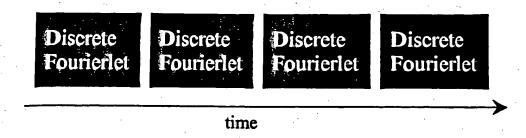


FIG. 12

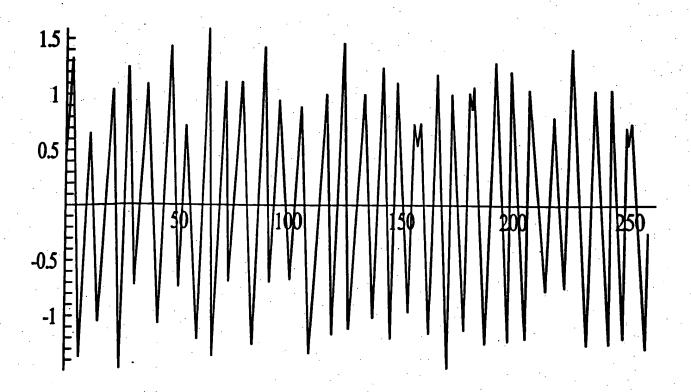


FIG. 13

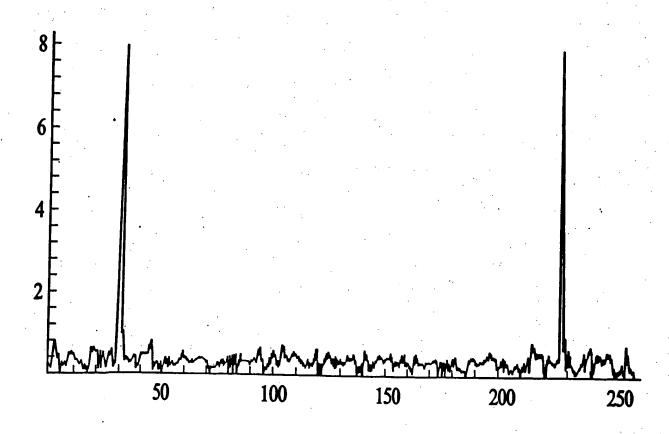


FIG. 14

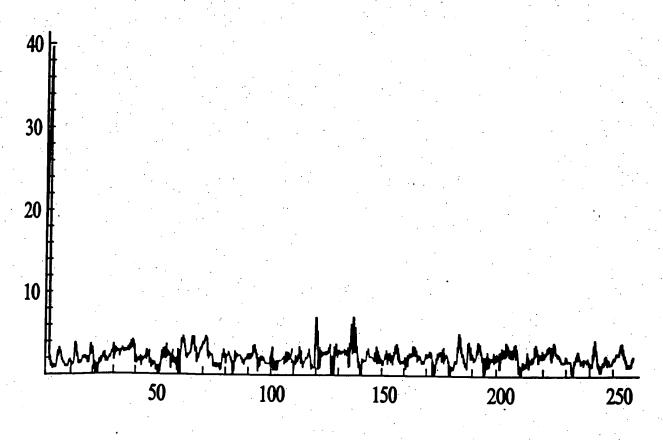


FIG. 15

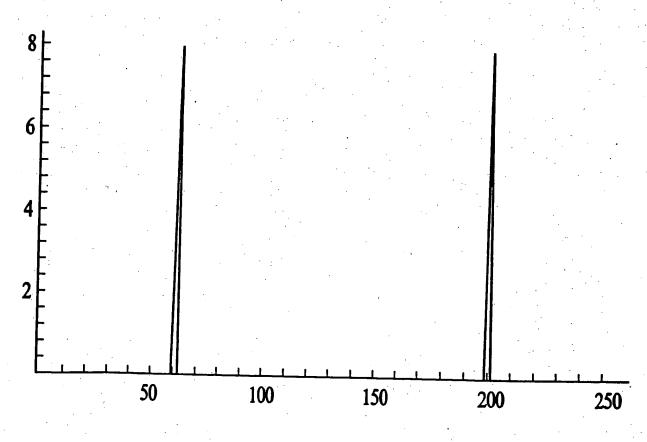
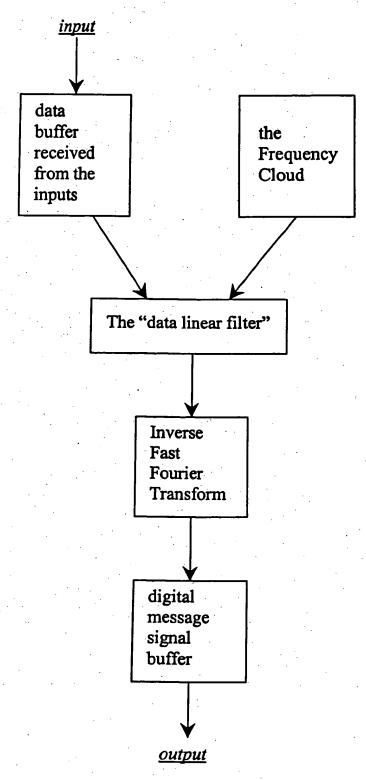
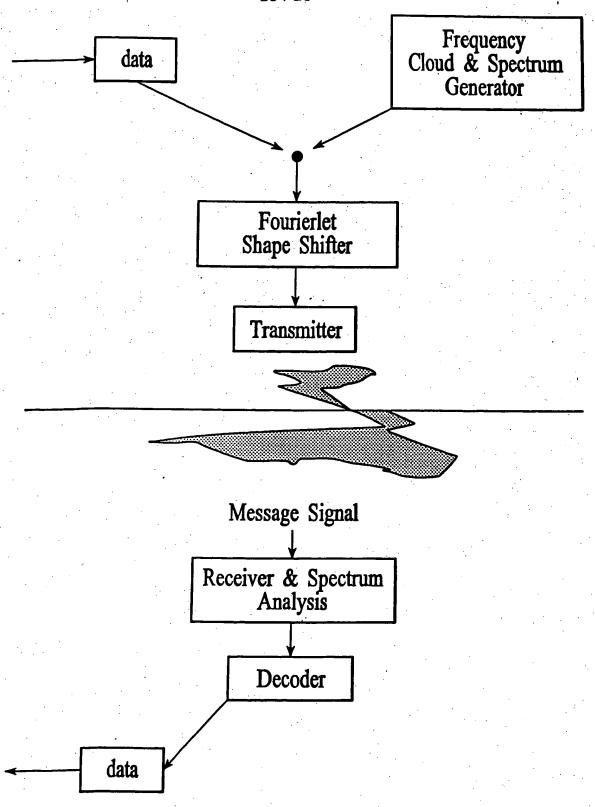


FIG. 16



The Fourierlet Shape Shifter

FIG. 17



The Fourierlet Shape Shifter in System Context.

FIG. 18

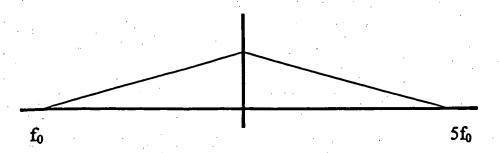


FIG. 19

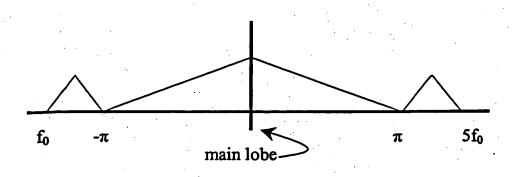


FIG. 22

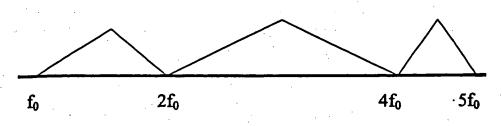


FIG. 23

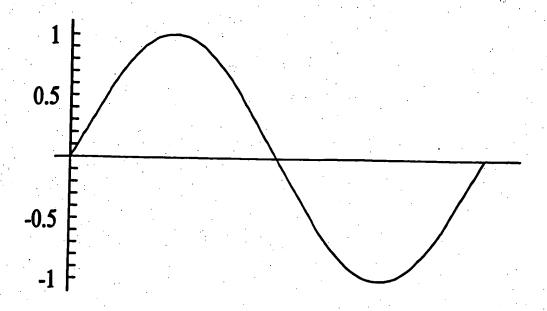


FIG. 20A

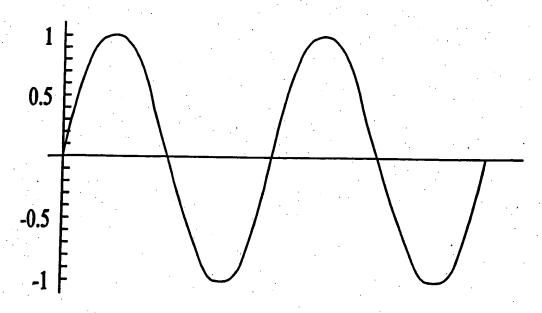


FIG. 20B

amplitude

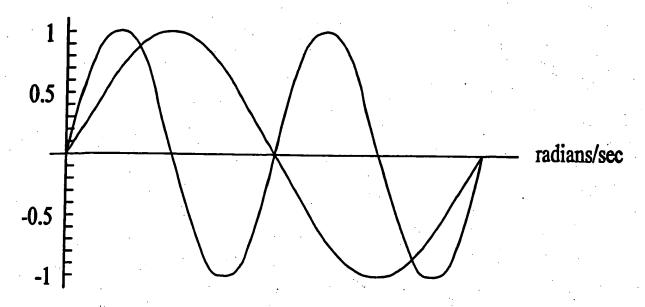


FIG. 20C

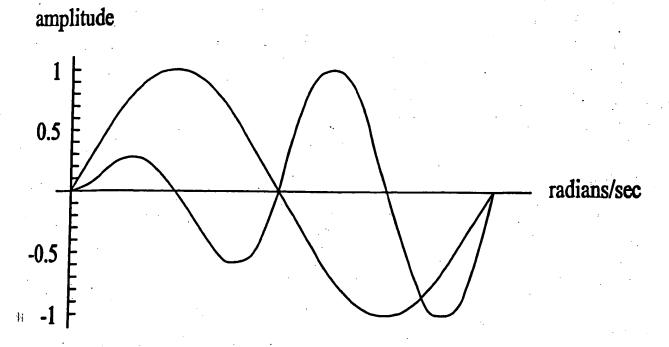


FIG. 21

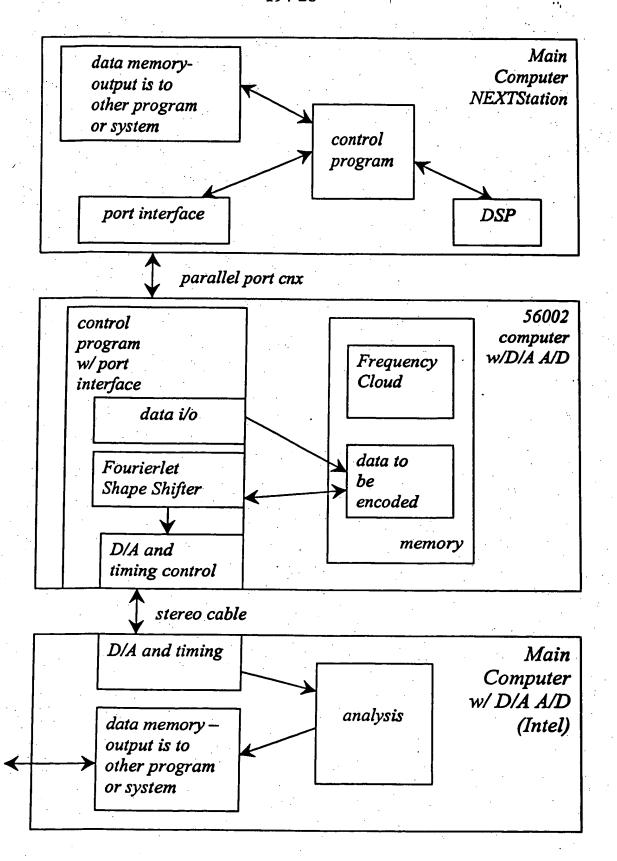


FIG. 24

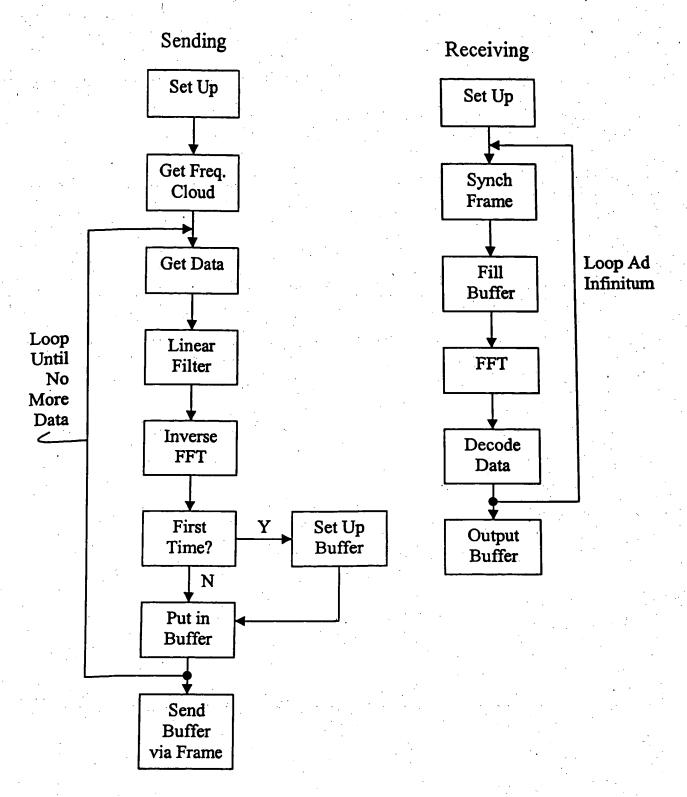


FIG. 25

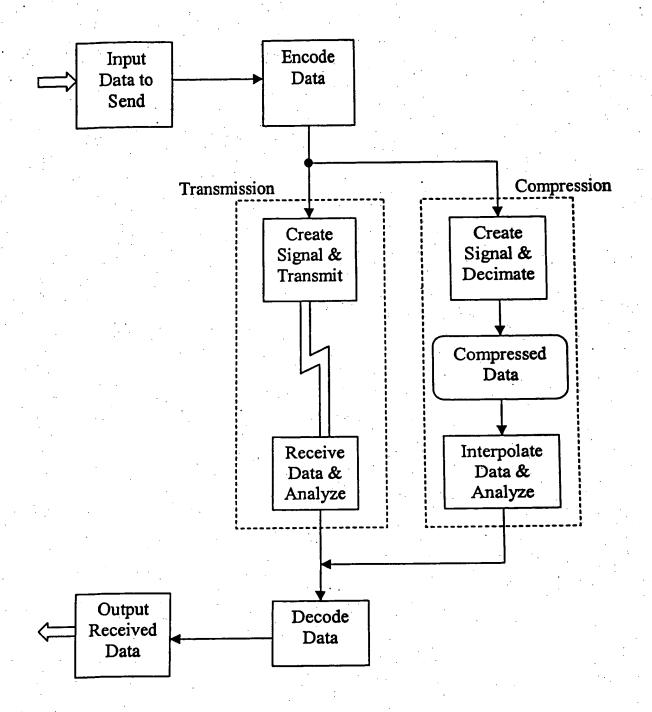


FIG. 26

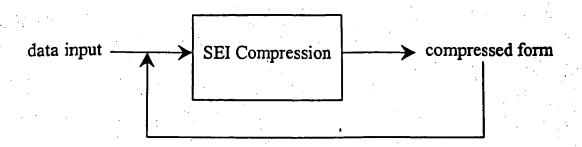


FIG. 27

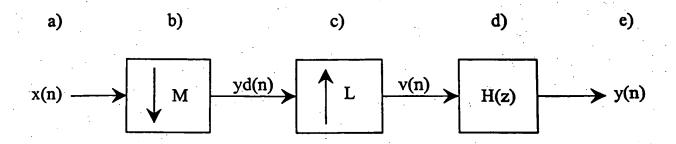


FIG. 28

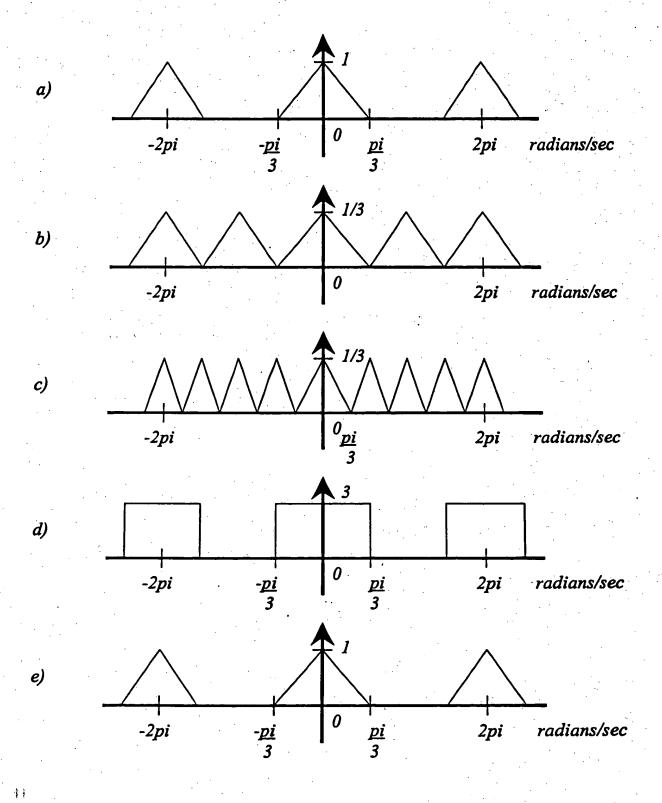


FIG. 29

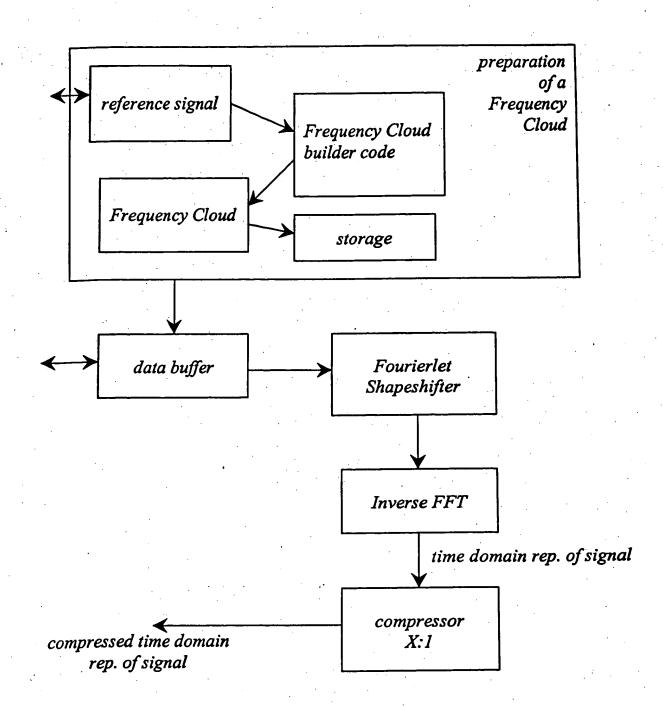


FIG. 30

11

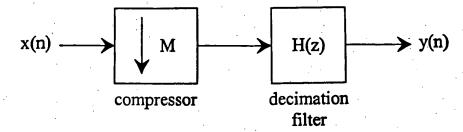


FIG. 31

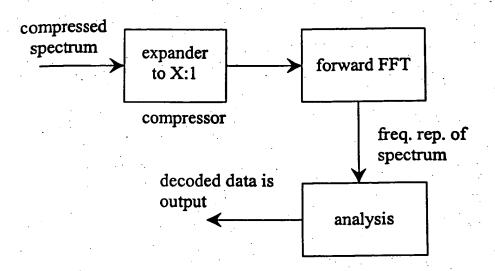


FIG. 32

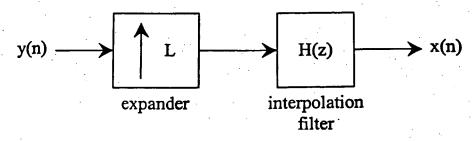


FIG. 33

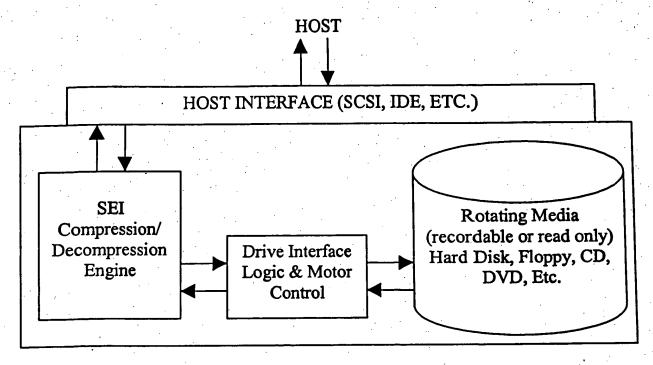


FIG. 34

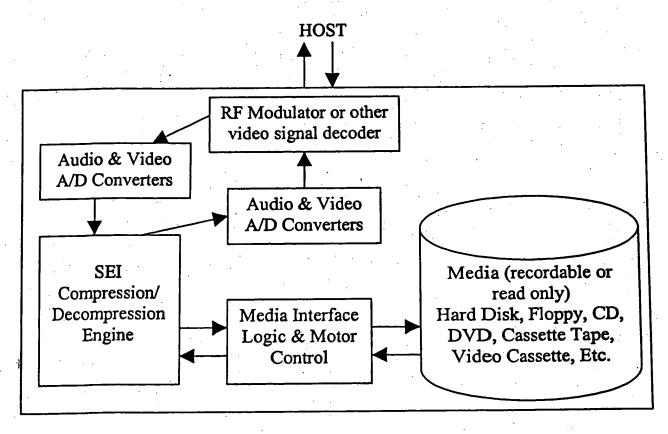


FIG. 35

FIG. 36 rest of device receive the hardware signal decode the data DSP Core The "Airwaves" and interface lines / logic A/D D/A CODEC System I/O Memory existing broadcast encode the data many I/O lines hardware to host system send to tx possible line out line in • • to send

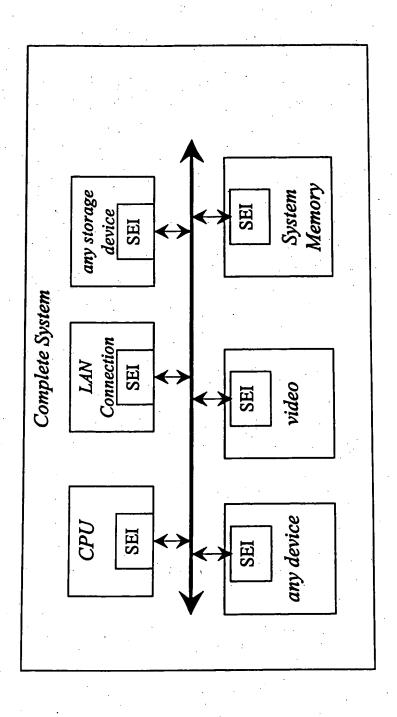


FIG. 38